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OUSD(R&E) Microelectronics Overview

Data and Metrics for the DOD SBIR and STTR Programs: A Workshop

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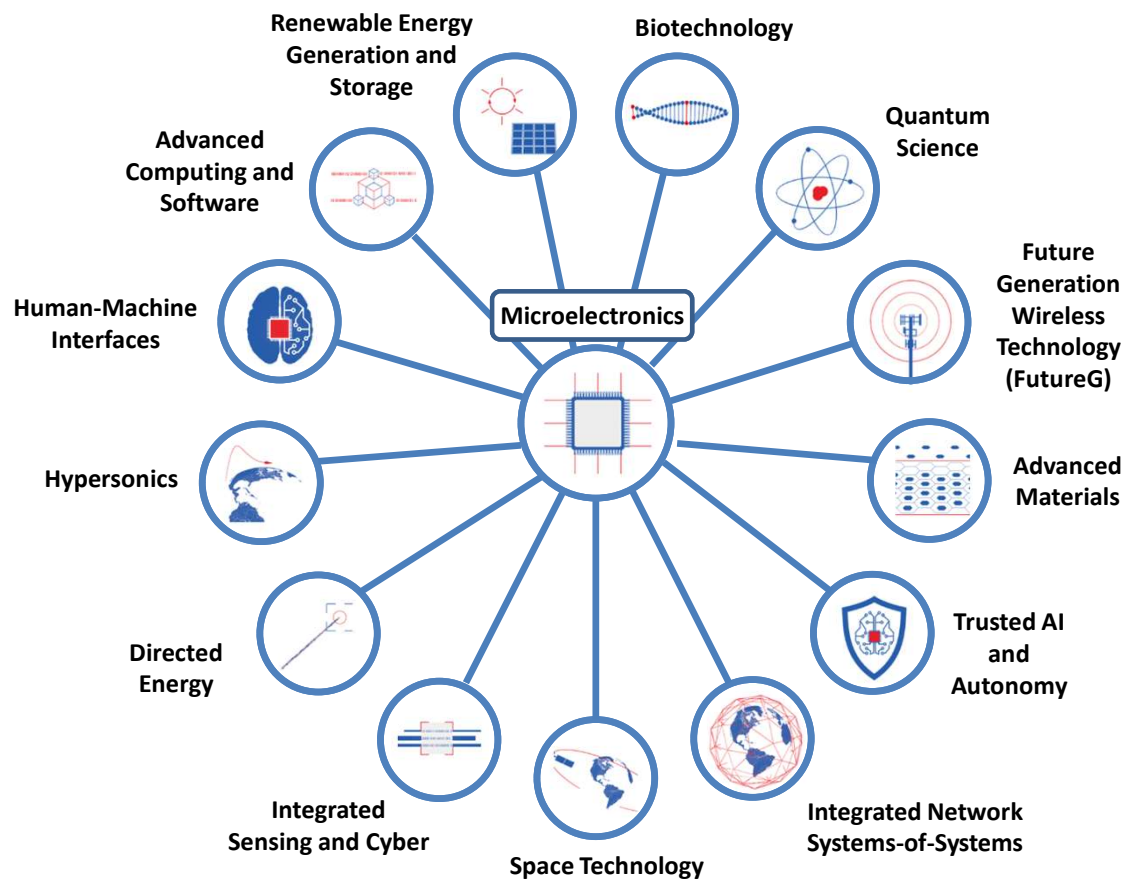
@DODCTO



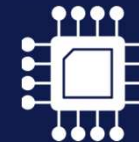
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CRITICAL TECHNOLOGY SYNERGIES: MICROELECTRONICS



Autonomous Drones



AI Chips



Self Driving Vehicles

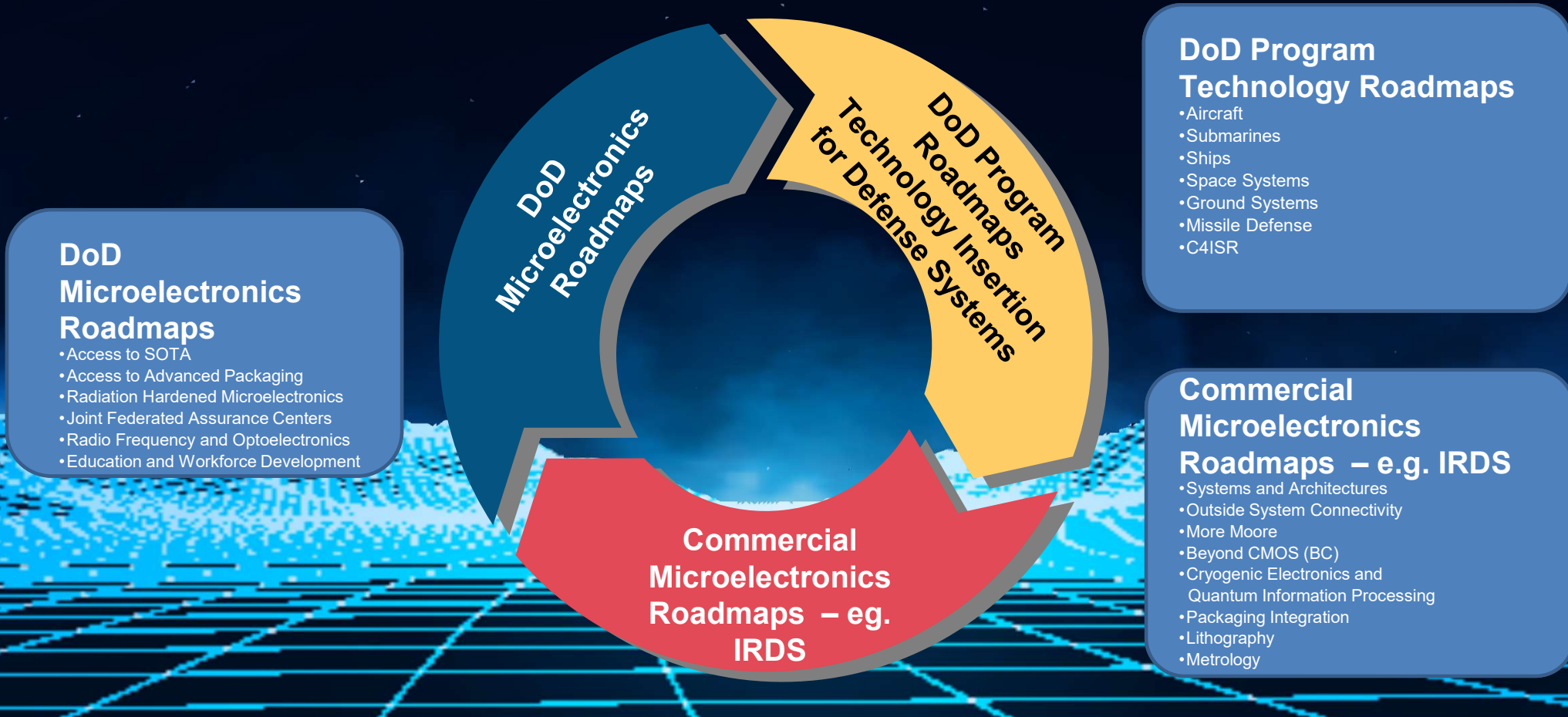


Energy Efficiency For Edge Computing

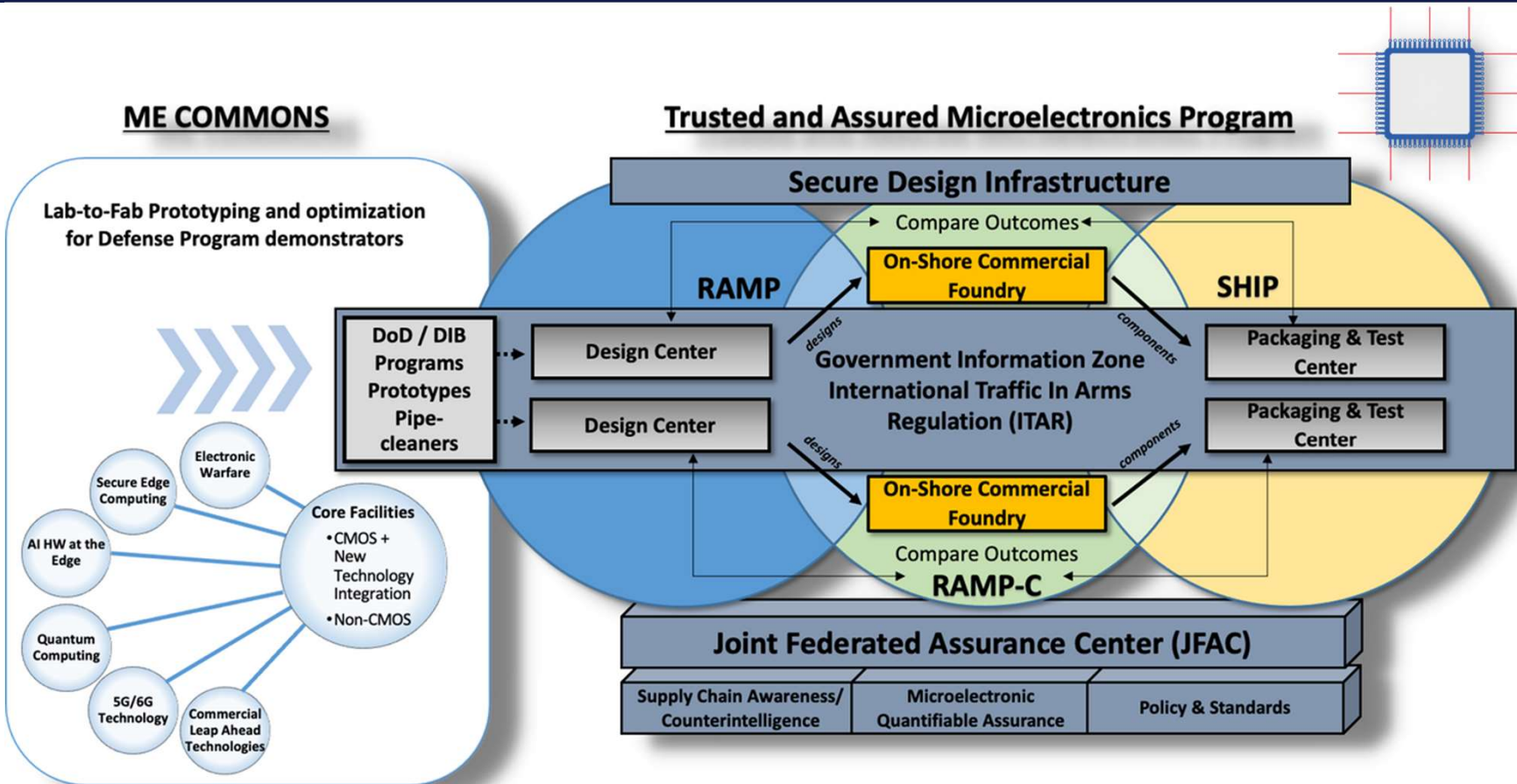


Military Applications

Connecting Commercial Technology to DoD Microelectronics Roadmaps and Defense Programs



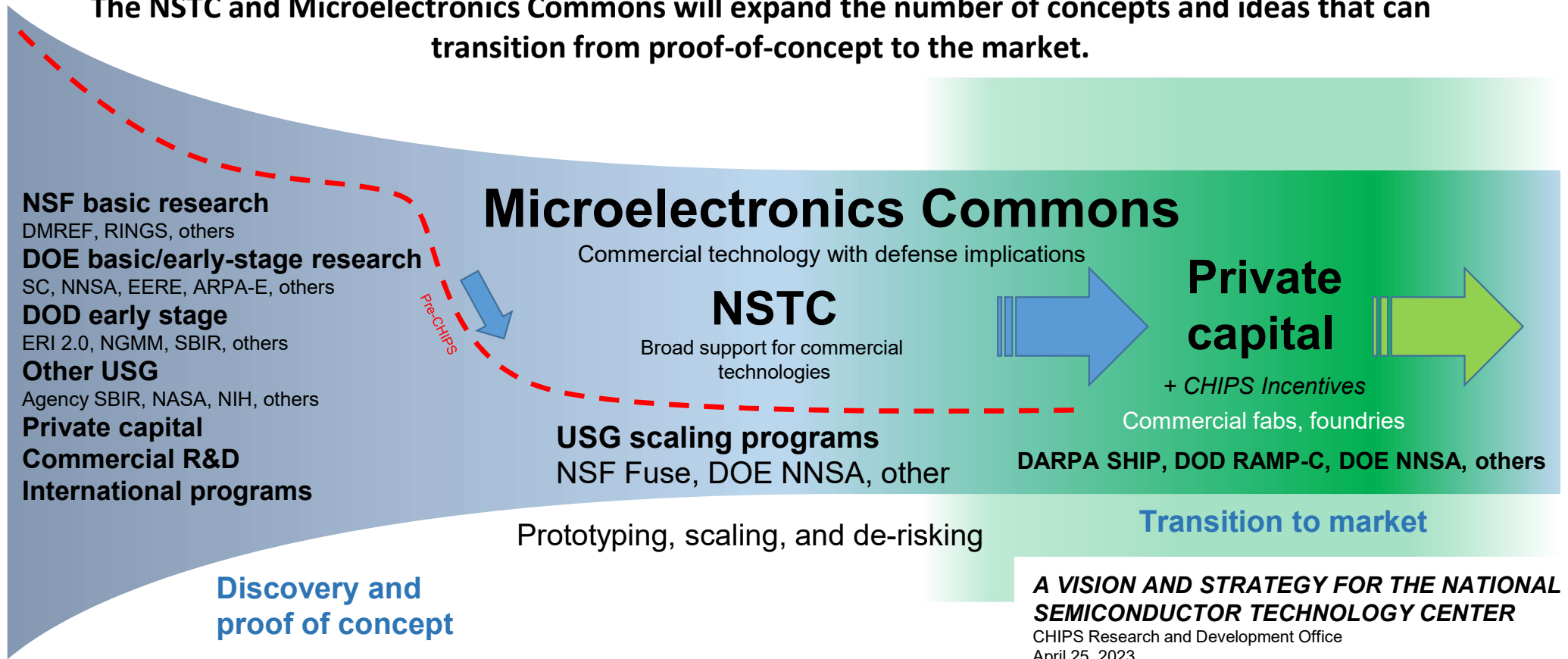
T&AM Program Enabling Access to State of the Art (SOTA)





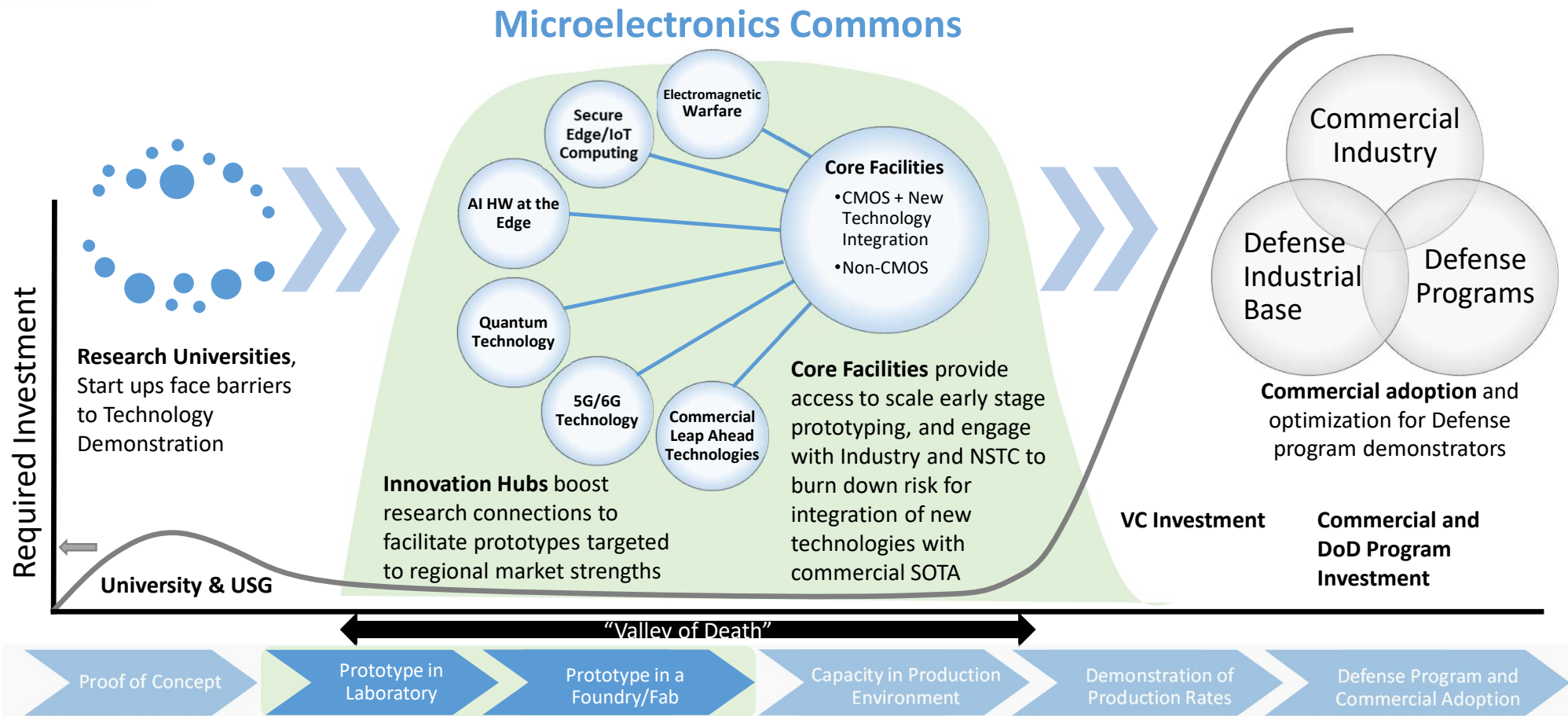
CHIPS Offers a Whole of Government Approach

The NSTC and Microelectronics Commons will expand the number of concepts and ideas that can transition from proof-of-concept to the market.





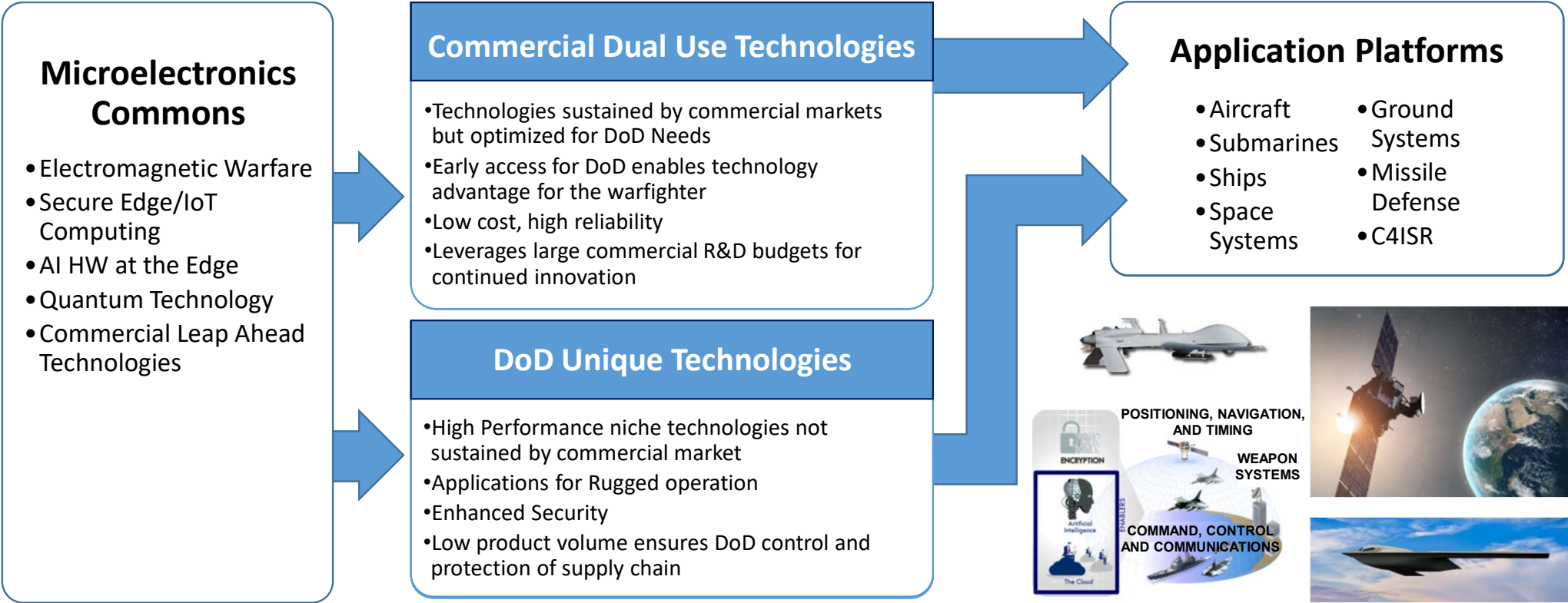
Microelectronics Commons Addresses the Valley of Death





Progression from Concept to Capabilities

MICROELECTRONICS COMMONS PATHWAYS TO DEVELOP AND DELIVER NEW DEFENSE CAPABILITIES



Lab-to-fab prototyping bridges valley of death from laboratory research to foundry/fab prototyping